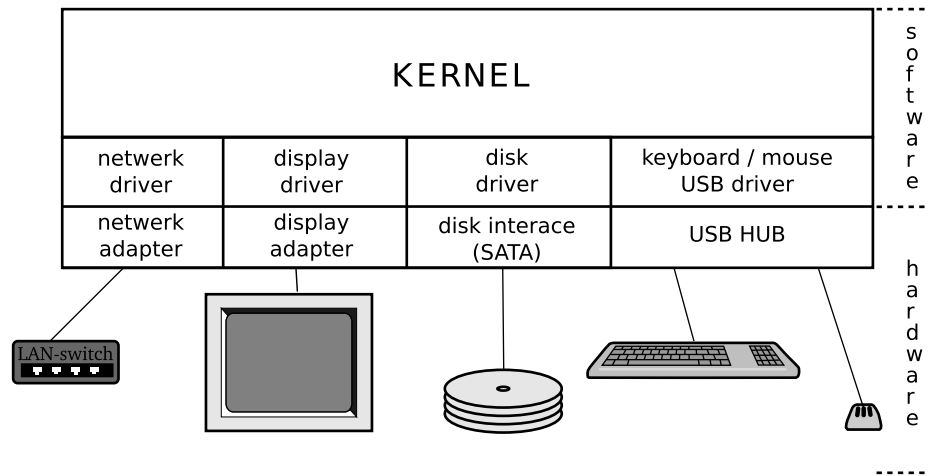


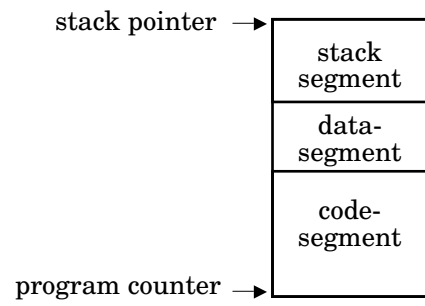
Hoofdstuk 12

Operating systems



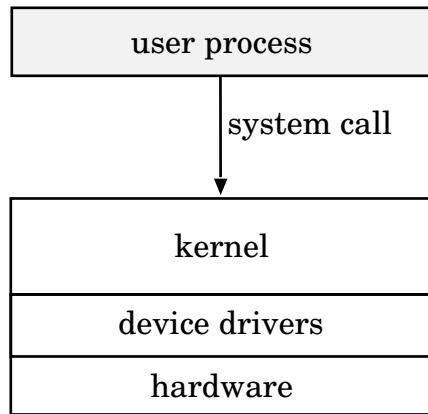
Figuur 12.1: Kernel met device drivers.

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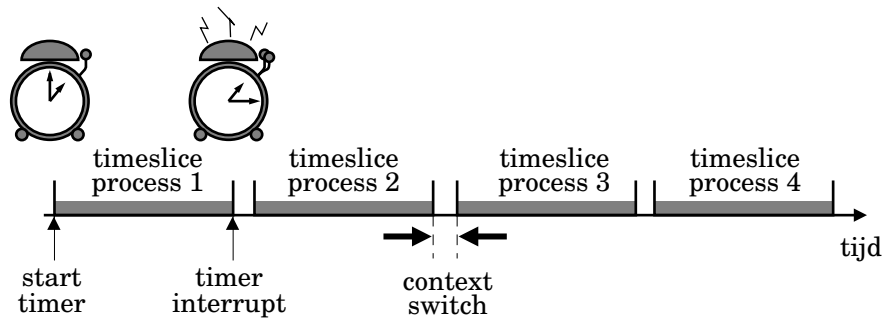
Figuur 12.2: Geheugenmodel van een process.

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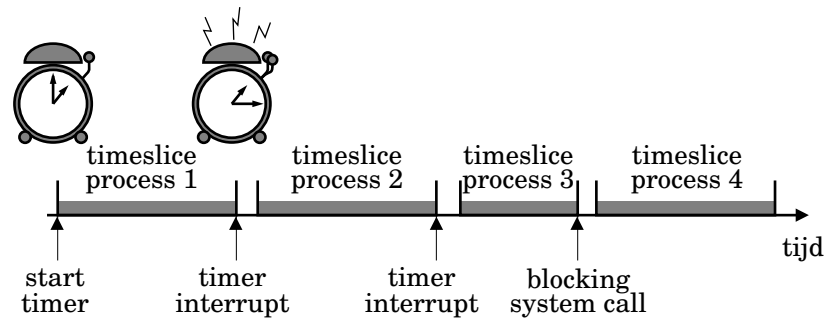


Figuur 12.3: Communicatie tussen user process en kernel.

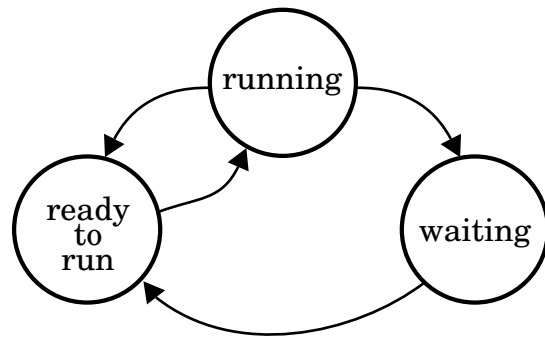
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Figuur 12.4: Pre-emptive scheduler.

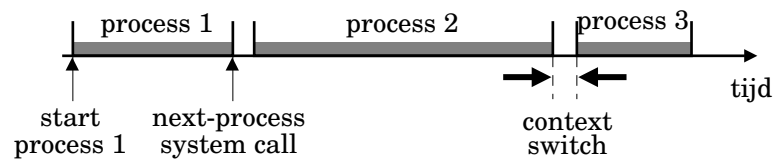


Figuur 12.5: Pre-emptive scheduler met blocking system call.



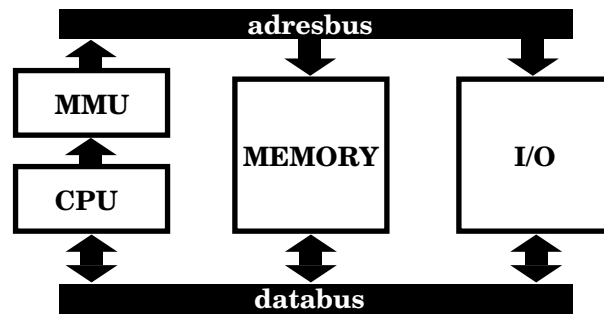
Figuur 12.6: Toestandsdiagram voor een proces.

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Figuur 12.7: Non-pre-emptive scheduler.

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Figuur 12.8: Plaats van de MMU.

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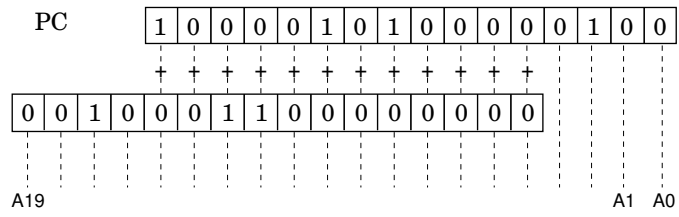
8088 -registers

PSW	FLAGS-H	FLAGS-L	AX	AH	AL
IP	PROGRAM COUNTER		BX	BH	BL
CS	CODE SEGMENT		CX	CH	CL
SS	STACK SEGMENT		DX	DH	DL
DS	DATA SEGMENT		SP	STACK POINTER	
ES	EXTRA SEGMENT		BP	BASE POINTER	
			DI	DESTINATION INDEX	
			SI	SOURCE INDEX	

Figuur 12.9: 8088-CPU-registerset.

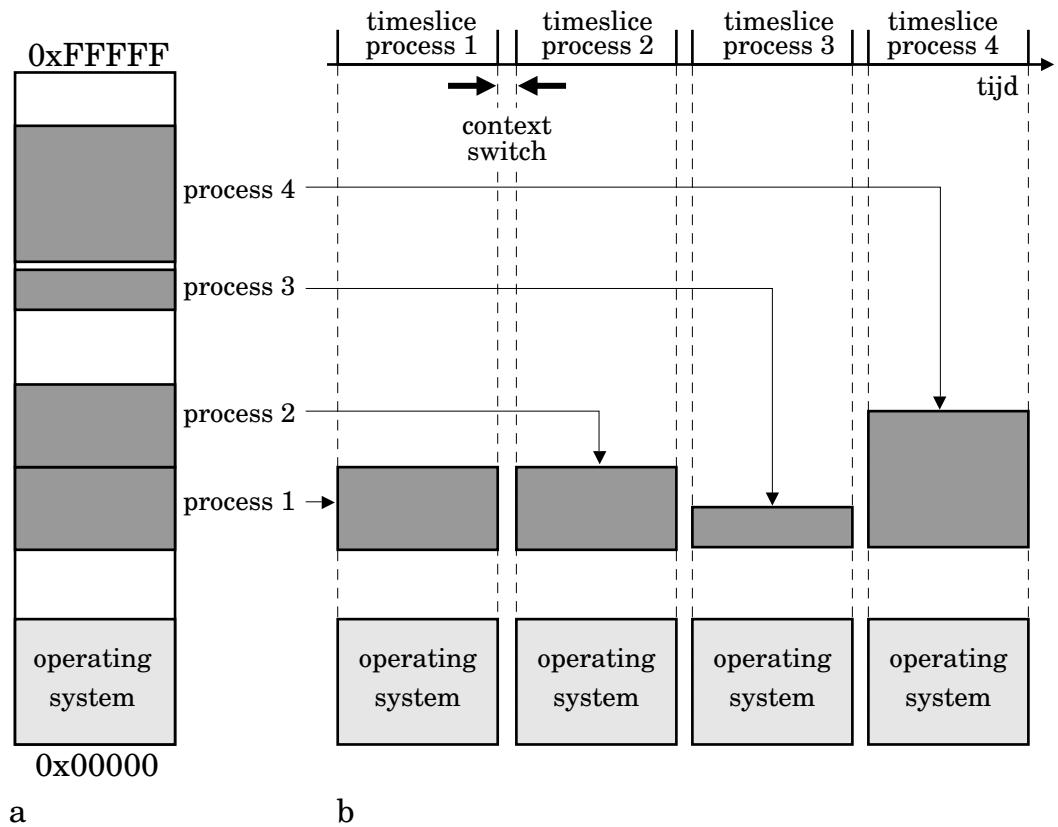
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8088 segment-register

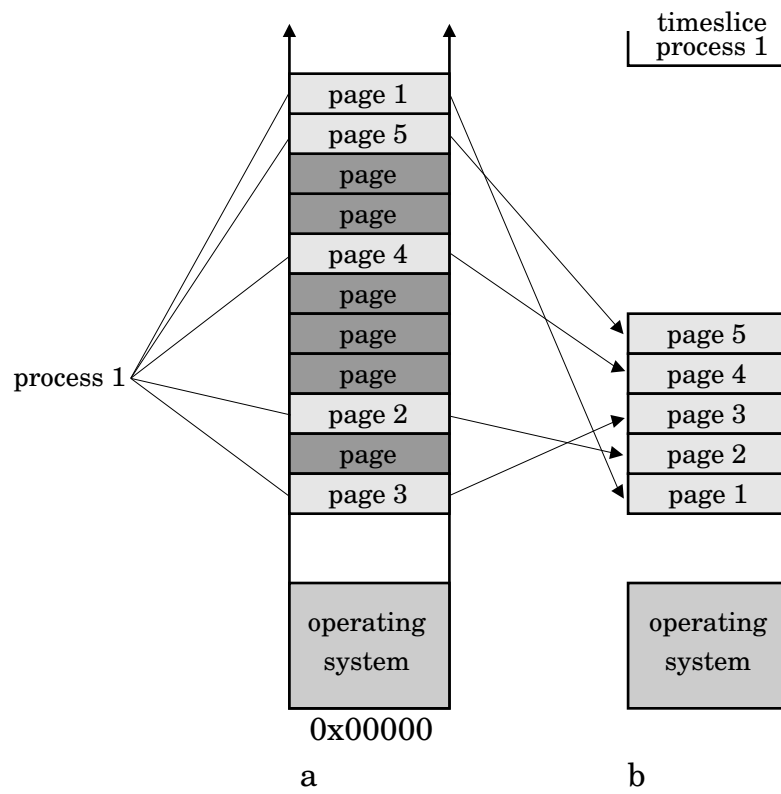


Figuur 12.10: Werking van de segmentregisters in de 8088.

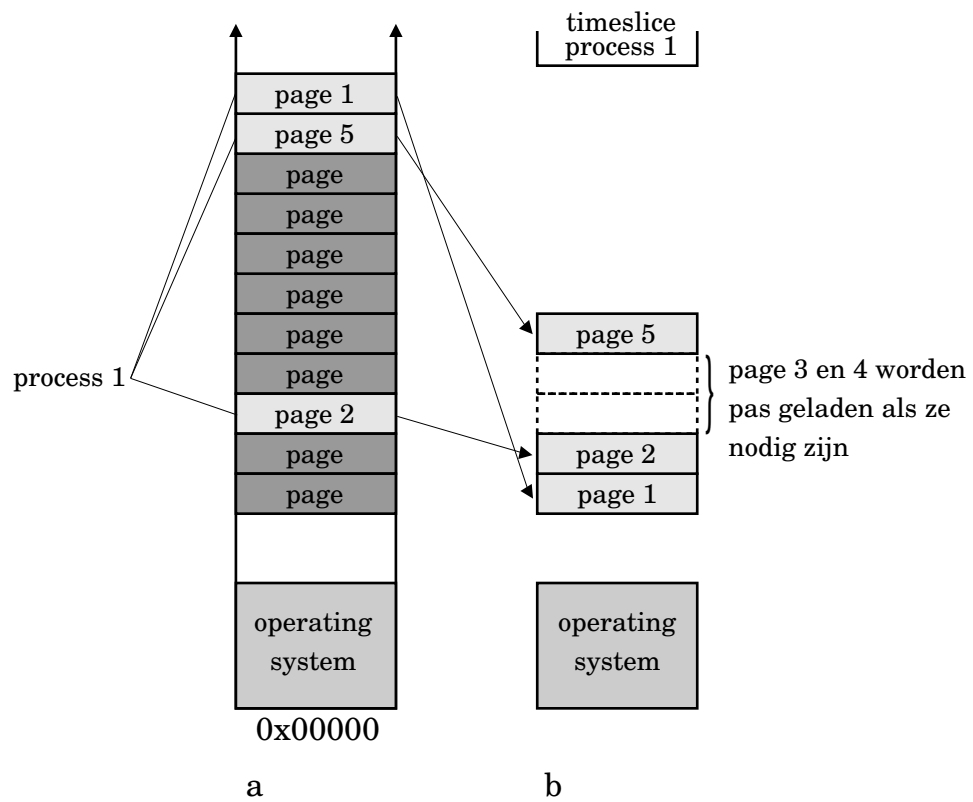
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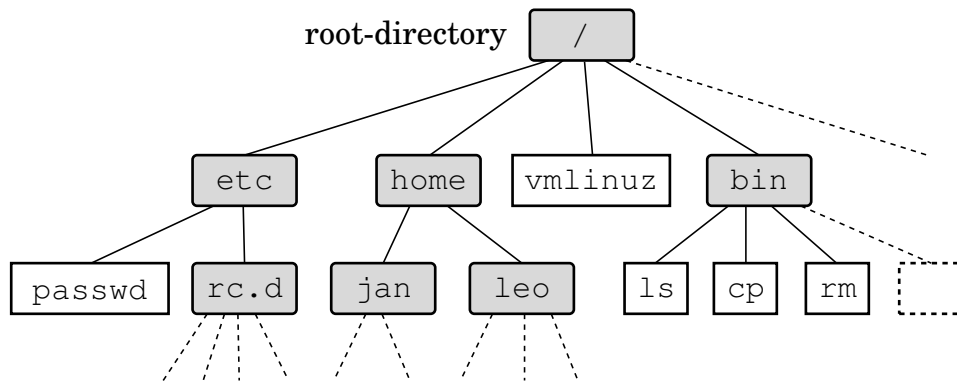
Figuur 12.11: Invloed van de MMU op de positie van processen: a) Memory map b) Memory zoals gezien door de CPU via de MMU op verschillende tijdstippen.



Figuur 12.12: Pagingsysteem: a) Memory map met pages b) gezien door de CPU via de MMU.

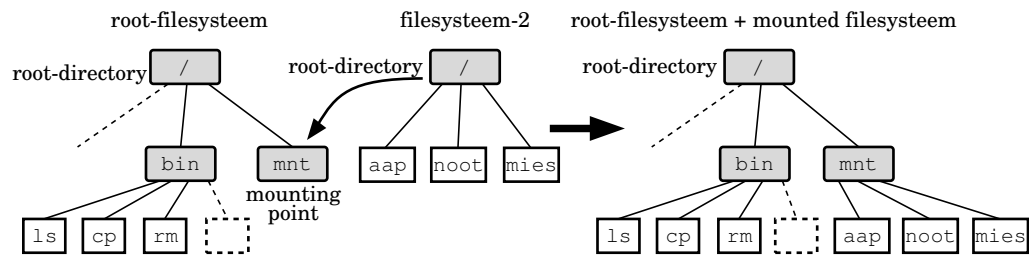


Figuur 12.13: Demand paging: a) Memory map met pages b) Gezien door de CPU via de MMU.



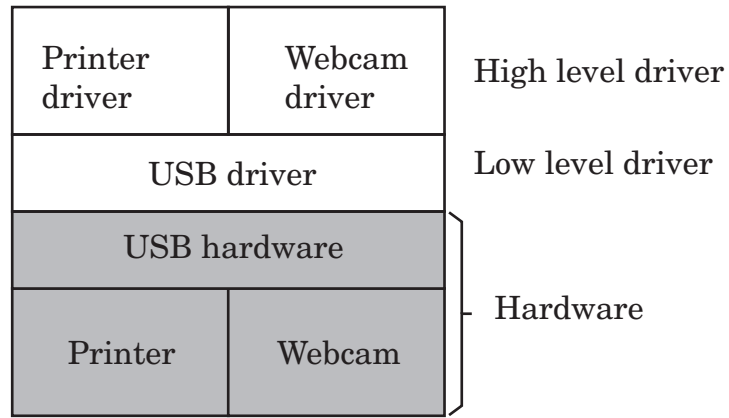
Figuur 12.14: Hierarchische organisatie van files.

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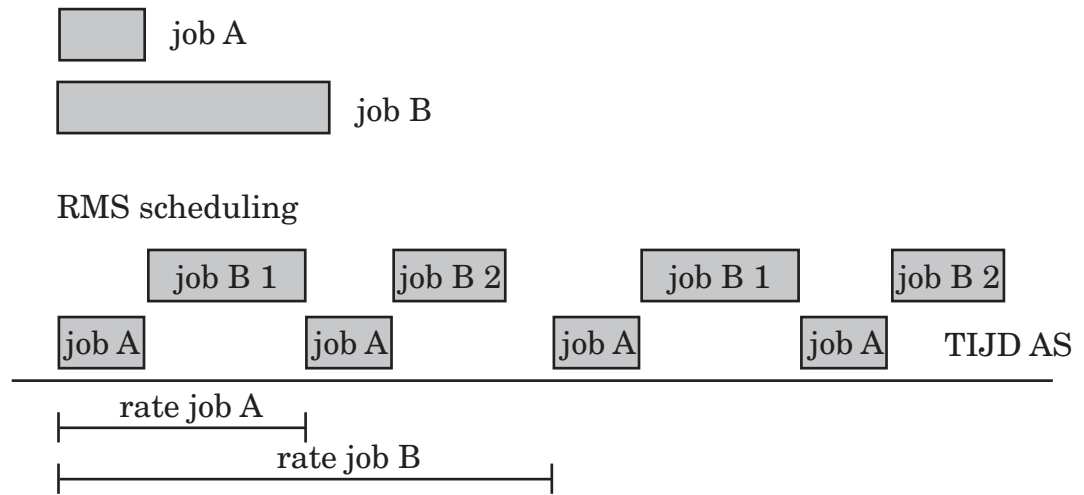
Figuur 12.15: Mounten van een filesystem.

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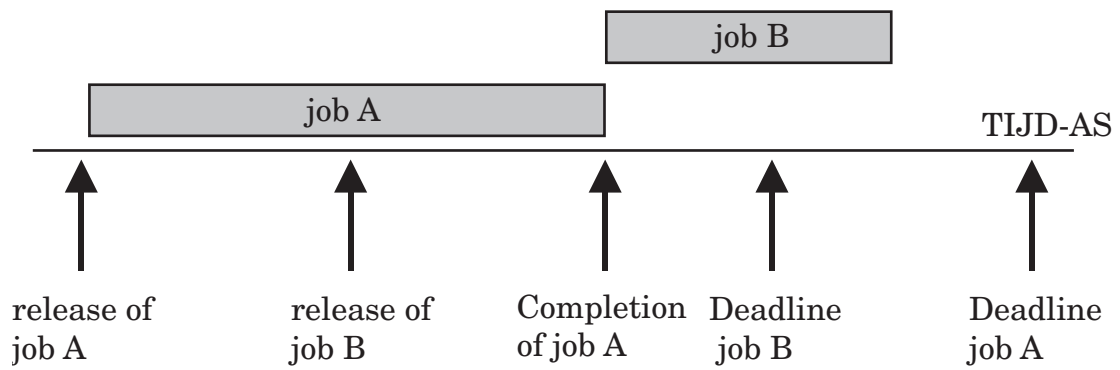


Figuur 12.16: Low-level en high-level driver.

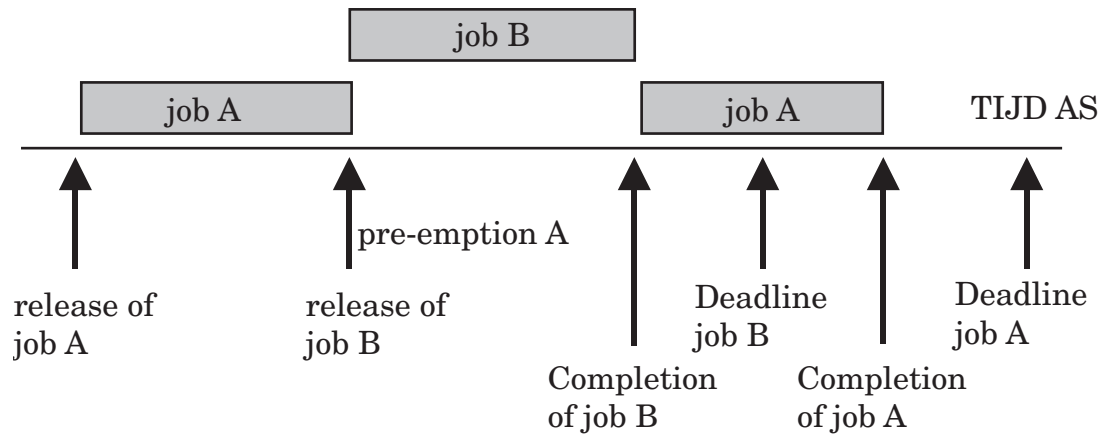
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Figuur 12.17: Haalbare RMS scheduling van twee jobs.



Figuur 12.18: EDF zonder pre-emption met gemiste deadline.



Figuur 12.19: EDF met pre-emption en gehaalde deadline.

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